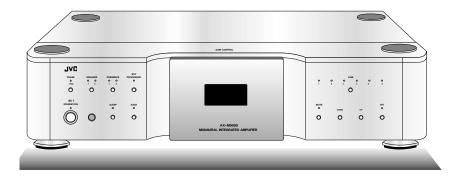
# **JVC** SERVICE MANUAL

## MONAURAL INTEGRATED AMPLIFIER

## AX-M9000

Area Suffix

E ----- Continental Europe



## Contents

Safety precautions	1-2
Disassembly method	1-3
Adjustment method	1-10
Description of major ICs	1-11~21

## -Safety Precautions

- 1. This design of this product contains special hardware and many circuits and components specially for safety purposes. For continued protection, no changes should be made to the original design unless authorized in writing by the manufacturer. Replacement parts must be identical to those used in the original circuits. Services should be performed by qualified personnel only.
- 2. Alterations of the design or circuitry of the product should not be made. Any design alterations of the product should not be made. Any design alterations or additions will void the manufacturer's warranty and will further relieve the manufacture of responsibility for personal injury or property damage resulting therefrom.
- 3. Many electrical and mechanical parts in the products have special safety-related characteristics. These characteristics are often not evident from visual inspection nor can the protection afforded by them necessarily be obtained by using replacement components rated for higher voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in the Parts List of Service Manual. Electrical components having such features are identified by shading on the schematics and by (A) on the Parts List in the Service Manual. The use of a substitute replacement which does not have the same safety characteristics as the recommended replacement parts shown in the Parts List of Service Manual may create shock, fire, or other hazards.
- 4. The leads in the products are routed and dressed with ties, clamps, tubings, barriers and the like to be separated from live parts, high temperature parts, moving parts and/or sharp edges for the prevention of electric shock and fire hazard. When service is required, the original lead routing and dress should be observed, and it should be confirmed that they have been returned to normal, after re-assembling.
- 5. Leakage currnet check (Electrical shock hazard testing)

After re-assembling the product, always perform an isolation check on the exposed metal parts of the product (antenna terminals, knobs, metal cabinet, screw heads, headphone jack, control shafts, etc.) to be sure the product is safe to operate without danger of electrical shock.

Do not use a line isolation transformer during this check.

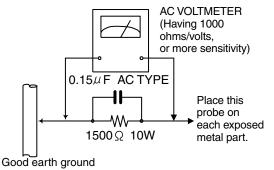
Plug the AC line cord directly into the AC outlet. Using a "Leakage Current Tester", measure the leakage current from each exposed metal parts of the cabinet, particularly any exposed metal part having a return path to the chassis, to a known good earth ground. Any leakage current must not exceed 0.5mA AC (r.m.s.).

#### Alternate check method

Plug the AC line cord directly into the AC outlet. Use an AC voltmeter having, 1,000 ohms per volt or more sensitivity in the following manner. Connect a 1,500  $\Omega$  10W resistor paralleled by a 0.15 $\mu$ F AC-type capacitor

between an exposed metal part and a known good earth ground. Measure the AC voltage across the resistor with the AC voltmeter.

Move the resistor connection to eachexposed metal part, particularly any exposed metal part having a return path to the chassis, and meausre the AC voltage across the resistor. Now, reverse the plug in the AC outlet and repeat each measurement. voltage measured Any must not exceed 0.75 V AC (r.m.s.). This corresponds to 0.5 mA AC (r.m.s.).



## Warning

- 1. This equipment has been designed and manufactured to meet international safety standards.
- 2. It is the legal responsibility of the repairer to ensure that these safety standards are maintained.
- 3. Repairs must be made in accordance with the relevant safety standards.
- 4. It is essential that safety critical components are replaced by approved parts.
- 5. If mains voltage selector is provided, check setting for local voltage.

Burrs formed during molding may be left over on some parts of the chassis. Therefore, pay attention to such burrs in the case of preforming repair of this system.

In regard with component parts appearing on the silk-screen printed side (parts side) of the PWB diagrams, the parts that are printed over with black such as the resistor (--), diode (+-) and ICP (-) or identified by the " $\underline{\wedge}$ " mark nearby are critical for safety.

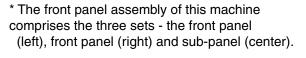
When replacing them, be sure to use the parts of the same type and rating as specified by the manufacturer. (Except the JC version)

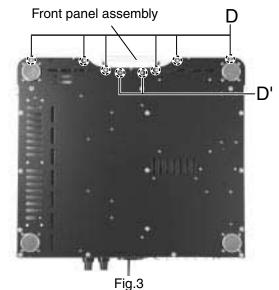
## **Disassembly method** ■Removing the metal cover (See Fig. 1)

- 1. Remove the three screws A attaching the metal cover on the back of the body.
- 2. Remove the four screws B attaching the metal cover on both sides of the body using a hexagon wrench.
- 3. Draw the metal cover upward from the behind while pulling the both sides of the lower part of the metal cover outward.

## Removing the front panel assembly (See Fig. 2 to 5)

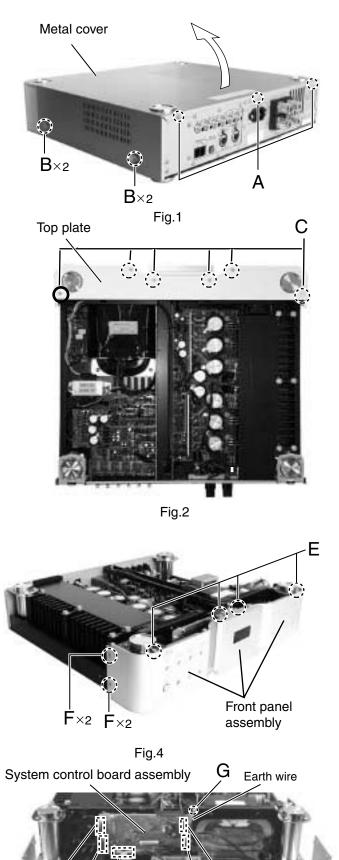
- Prior to performing the following procedure, remove the metal cover.
- 1. Remove the six screws C attaching the top plate using a hexagon wrench.
- 2. Remove the six screws D and tow screws D' attaching the front panel assembly from the back of the body.
- 3. Remove the four screws E attaching the front panel assembly from the upper part of the body.
- 4. Remove the four screws F attaching the front panel assembly on both sides of the body.
- 5. Disconnect the harness from the connectors CN601, CN602, CN603, CN604 and CN605 of the system control board assembly.
- 6. Remove the screw G attaching the earth wire.





CN602 CN601

CN605



1-3

CN604

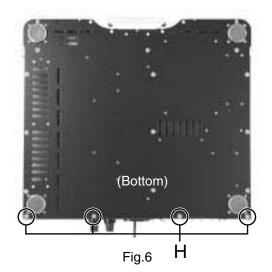
CN603

Fig.5

## Removing the rear panel (See Fig.6 to 9)

- Prior to performing the following procedure, remove the metal cover and front panel assembly.
- 1. Remove the four screws H attaching the rear panel on the back of the set.
- 2. Remove the eight screws I attaching the rear panel on the upper part of the set.
- 3. Remove the six screws J, two screws K, nine screws L and one screw M attaching the rear panel.
- 4. Draw the rear panel upward from the behind at the legs while pulling the rear panel outward.
- 5. Remove the four screws N attaching the speaker output cord.

\* If necessary, remove the power plug, connector clamp, etc.



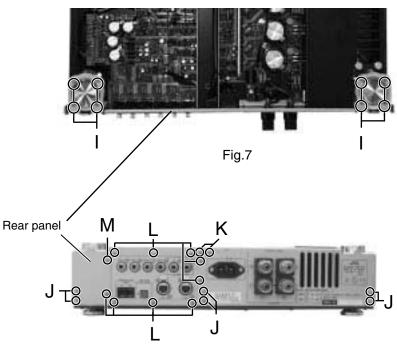
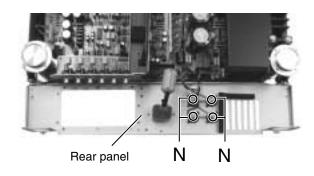


Fig.8

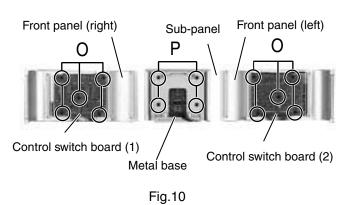




## <Removing the front panel section>

## Removing the control switch board assembly 1 & 2 and display board assembly (See Fig.10 to 11)

- Prior to performing the following procedure, remove the metal cover and front panel assembly.
- 1. Remove the ten screws O attaching the control switch board assembly 1 & 2 on the back side of the front panel assembly (left & right sides).
- 2.
  - Remove the four screws P attaching the metal base on the backside of the sub-panel assembly (center).
- Remove the four screws Q on the backside of the metal base. Then the display board assembly can be removed.



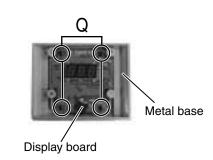


Fig.11

## Removing the system control board assembly (See Fig. 12)

- Prior to performing the following procedure, remove the metal cover and front panel assembly.
- 1. Remove the four screws R attaching the system control board assembly.
- 2. Disconnect the connectors CN611, CN612, CN613, CN614, CN615 and CN616.

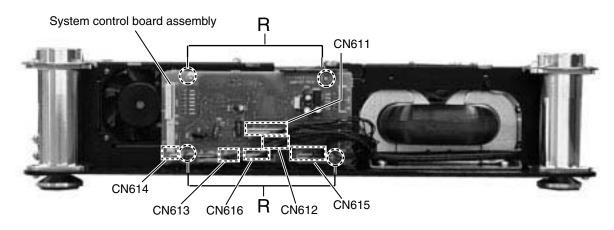
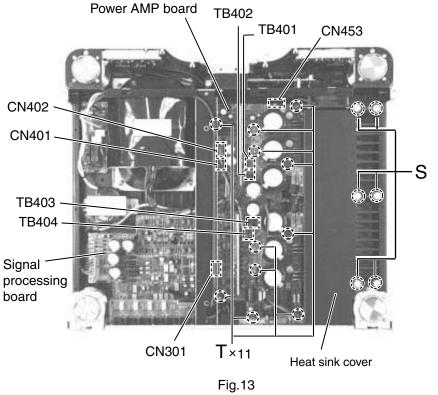


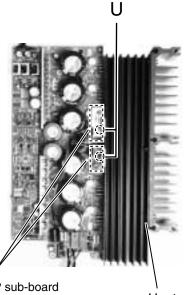
Fig.12

## AX-M9000



## Removing the power AMP board assembly / heat sink (See Fig. 13 to 15)

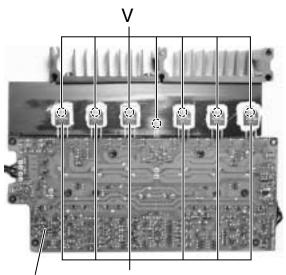
- Prior to performing the following procedure, remove the metal cover, system control assembly and rear cover.
- 1. Remove the six through bolts S attaching the heat sink and the heat sink cover.
- 2. Remove the eleven screws T attaching the power AMP sub-board assembly.
- 3. Disconnect the harness, which is extending from power transformer, from the connectors TB401, TB402, TB403 and TB404.
- 4. Disconnect the harness, which is extending from the power transformer for pre-AMP, from the connectors CN401 and CN402.
- 5. Disconnect the harness, which is extending from the fan, from the connector CN453.
- 6. Disconnect the harness, which is extending from the signal processing board, from the connector CN301.
- After removing each screw U attaching the power AMP sub-board assembly (1) & (2), take off the power AMP sub-board assembly from the heat sink.
- 8. Remove the seven screws V attaching transistor Q335~Q340.



Power AMP sub-board assembly (1) (2)

Heat sink





Power AMP board

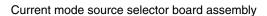
Transisitor Q335~Q340.

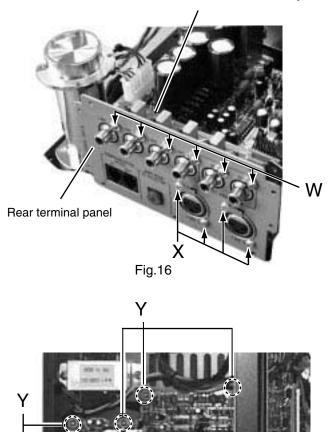
Removing the current mode source selector board assembly and rear terminal panel (See Fig. 16)

- Prior to performing the following procedure, remove the metal cover and the rear panel.
- Remove the six screws W attaching the RCA pins on the current mode source selector board assembly. Then, the current mode source selector board can be removed.
- 2.

Remove the four screws X attaching the cannon connector on the signal processing board.

3. Then, the rear terminal panel can be removed.







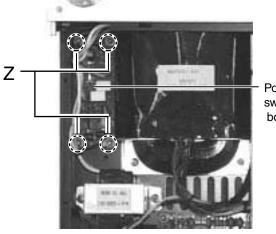
Signal processing board

## Removing the signal processing board assembly (See Fig. 17)

- Prior to performing the following procedure, remove the metal cover and the rear panel.
- 1. Remove the current mode source selector board assembly and the rear terminal panel.
- 2. Remove the five screws Y attaching the signal processing board assembly.
- 3. Remove the connectors CN251 and CN252.



- Prior to performing the following procedure, remove the metal cover.
- 1. Remove the four screws Z attaching the power switch board assembly.
- 2. Remove the cable connecting to the transformer.

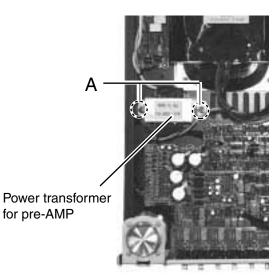


Power switch board



## AX-M9000

- Removing the core transformer for pre-AMP (See Fig. 19)
- · Prior to performing the following procedure, remove the metal cover.
- 1. Remove the two screws A attaching the power transformer for pre-AMP.



## for power AMP

- · Prior to performing the following procedure, remove the metal cover and the front panel assembly.
- 1. Remove the four screws B attaching the power transformer for power AMP.

Fig.19

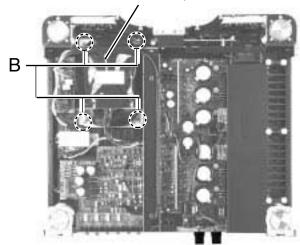


Fig.20

## Removing the transformer

(See Fig. 20)

## Power transformer for power AMP

## Arrangement drawings for the clamp binding

In order to secure the performance of the machine, refer to the following arrangement drawings when the lines are clamped.

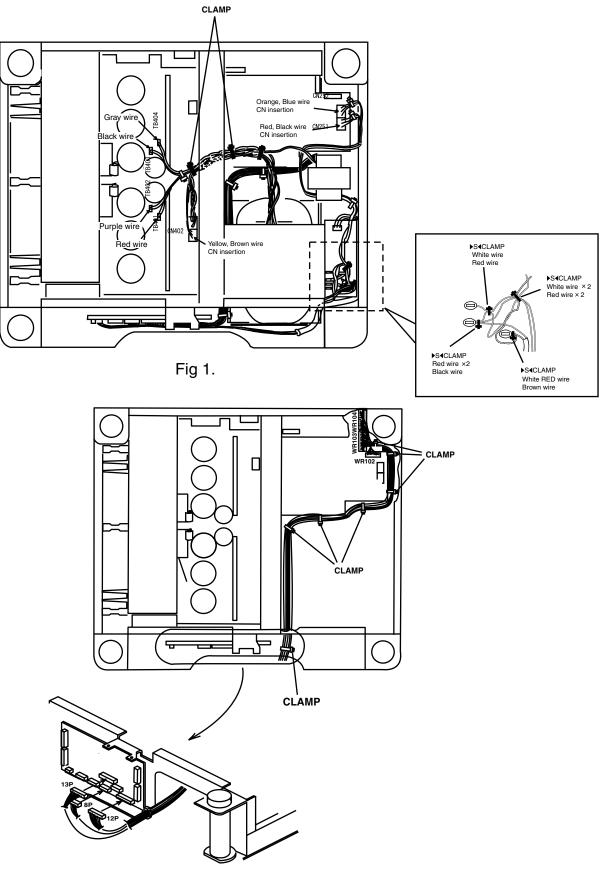


Fig 2.

## Adjustment method

## 1. List of measuring devices

Measuring devices : Electronic voltmeter

Oscilloscope

Low-frequency transmitter (50Hz-20 kHz of transmitting frequency and output of 0 dBs at the 600  $\Omega$  terminal)

Distortion factor tester (with a built-in band path filter)

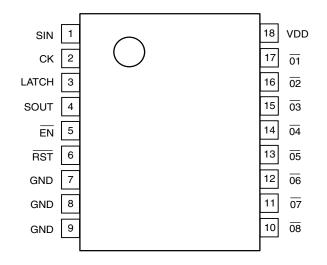
## 2. Adjustments and confirmation

Adjusting met	ime of THI or at the tir inputting 1 Input sh Volume 100Hz 1kHz 10kHz	ne of kHz 200mV ort	Standard va More than 210W/8Ω (41.0V 20Hz Less tha 1kHz Less tha 20kHz Less tha 192mV +/- 10m Less than 10mV Less than 0.4m 45.6dB 1dB 33.4dB 1dB AT 31.0dB 1dB 5.8dB 1dB 45.6dB 1dB 45.6dB 1dB	V) n 0.04% n 0.06% NV V	
) INPUT LINE1 - Distortion factor 200W (40.0V) at 8Ω load ) Output level at the time when it to LINE 1 ) Line 1 ) Base hum ) Line 1 ) No signal, no load, Vol. 0.0 ) Prior to turning the power on, to	or at the tir inputting 1 Input sh Volume 100Hz 1kHz 10kHz	ne of kHz 200mV ort min. Vol. 99. 5 Vol. 99. 5 Vol. 69. 5 Vol. 44. 0	210W/8Ω (41.0) 20Hz Less tha 1kHz Less tha 20kHz Less tha 192mV +/- 10m Less than 10m Less than 0.4m 45.6dB 1dB 33.4dB 1dB AT 31.0dB 1dB 5.8dB 1dB 45.6dB 1dB	In 0.04% n 0.04% n 0.06% NV V IV	
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to LINE 1 ) Line 1 ) Base hum ) Line 1 ) Line 1 ) No signal, no load, Vol. 0.0 ) Prior to turning the power on, t	Input sh Volume 100Hz 1kHz 10kHz	ort min. Vol. 99. 5 Vol. 99. 5 Vol. 69. 5 Vol. 44. 0	Less than 10m Less than 0.4m 45.6dB 1dB 33.4dB 1dB AT 31.0dB 1dB 5.8dB 1dB 45.6dB 1dB		
) Base hum ) Line 1 ) No signal, no load, Vol. 0.0 ) Prior to turning the power on, t	Volume 100Hz 1kHz 10kHz	min. Vol. 99. 5 Vol. 99. 5 Vol. 69. 5 Vol. 44. 0	Less than 0.4m 45.6dB 1dB 33.4dB 1dB AT 31.0dB 1dB AT 5.8dB 1dB 45.6dB 1dB		
) Line 1 ) No signal, no load, Vol. 0.0 ) Prior to turning the power on, t	100Hz 1kHz 10kHz	Vol. 99. 5 Vol. 99. 5 Vol. 69. 5 Vol. 44. 0	45.6dB 1dB 45.6dB 1dB 33.4dB 1dB AT 31.0dB 1dB 5.8dB 1dB 45.6dB 1dB	IT ON	
) No signal, no load, Vol. 0.0	1kHz 10kHz	Vol. 99. 5 Vol. 69. 5 Vol. 44. 0	45.6dB 1dB 33.4dB 1dB AT 31.0dB 1dB 5.8dB 1dB 45.6dB 1dB		
) Prior to turning the power on, t		Vol. 44. 0	5.8dB 1dB 45.6dB 1dB		
) Prior to turning the power on, t			45.6dB 1dB		
) Prior to turning the power on, t					
			1	mV	
	4 pin of th voltage to just the vo cklash, tho ake sure th here the vo				
) No signal, no load, rated powe onfirm under the room tempera			Five minutes later after turning the power on		
	1kHzMore that10kHzMore that100HzMore that1kHzMore that	an 70dB an 70dB an 70dB n 70dB			
f ≋ ⊡ ( i (	all within the specified value, i specified in 3) above. No signal, no load, rated powe nfirm under the room tempera INPUT LINE 1 - The output at nput terminal side shall be +2	all within the specified value, readjust it is specified in 3) above. No signal, no load, rated power nfirm under the room temperature of 20- INPUT LINE 1 - The output at the nput terminal side shall be +20dBm	No signal, no load, rated power nfirm under the room temperature of 20-25 °C INPUT LINE 1 - The output at the nput terminal side shall be +20dBm	iall within the specified value, readjust it to the values         specified in 3) above.         No signal, no load, rated power         nfirm under the room temperature of 20-25 °C         INPUT LINE 1 - The output at the         nput terminal side shall be +20dBm         as a standard.         Line1         100Hz         More that         100Hz         100Hz </td <td>iall within the specified value, readjust it to the values         specified in 3) above.         No signal, no load, rated power         nfirm under the room temperature of 20-25 °C         INPUT LINE 1 - The output at the         nput terminal side shall be +20dBm         as a standard.         Line1         100Hz         More than 70dB         10Hz         10Hz</td>	iall within the specified value, readjust it to the values         specified in 3) above.         No signal, no load, rated power         nfirm under the room temperature of 20-25 °C         INPUT LINE 1 - The output at the         nput terminal side shall be +20dBm         as a standard.         Line1         100Hz         More than 70dB         10Hz         10Hz

## **Description of major ICs**

## ■ BU2114F-X (IC542,543,544) : LED controller

#### 1. Pin layout



#### 2. Pin function

Pin No.	Symbol	I/O	Function
1	SIN	I	Serial data input terminal
2	СК	I	Shift lock of the shift register
3	LATCH	Ι	When this terminal is set at "L", the latch output will be secured, In addition, when the output of the shift register is changed during "H", the latch output will be changed simultaneously.
4	SOUT	0	Output of the shift register at the final stage
5	EN	I	Enable terminals of 01 to 08
6	RST	I	Reset of the shift register and latch
7	GND	-	Ground
8	GND	-	Ground
9	GND	-	Ground
10	08	0	Output of the shift register at the 8th stage
11	07	0	Output of the shift register at the 7th stage
12	06	0	Output of the shift register at the 6th stage
13	05	0	Output of the shift register at the 5th stage
14	04	0	Output of the shift register at the 4th stage
15	03	0	Output of the shift register at the 3rd stage
16	02	0	Output of the shift register at the 2nd stage
17	01	0	Output of the shift register at the 1st stage
18	VDD	-	Power supply

## ■ MN101CP10A(IC521) : System controller

1.Pin layout

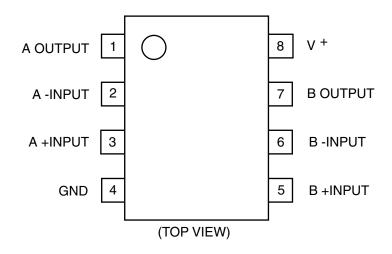
1	64	~	49	48	
٢				٢	
16	17	~	32	33	

2. Pin function

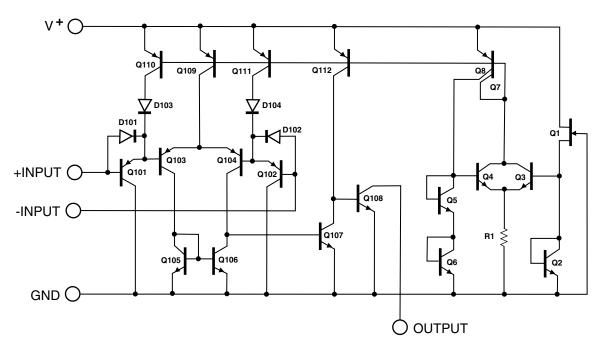
Pin No.	Symbol	I/O	Pin function	Pin No.	Symbol	I/O	Pin function
1	KY10 (DOWN)	Ι	Key input 10	32	ROT1	Ι	Rotary input 1
2	KY11 (LINE)	Ι	Key input 11	33	С	0	Line decoder C
3	KY12 (MUTE)	I	Key input 12	34	В	0	Line decoder B
4	TMP IN1	Ι	Temperature detect 1	35	Α	0	Line decoder A
5	TMP IN2	Ι	Temperature detect 2	36	POWER	0	Power SW
6	VREF+		AD reference voltage	37	ABS	0	Absolute phase SW
			terminal 5.0V	38	MONPRO	0	MON/ PRO SW
7	VDD		2.0 5.0V supply	39	ALLEACH	0	All each (LED)
8	OSC2	0	Clock out	40	PRESEN1	0	Presence SW1
9	OSC1	Ι	Clock in	41	PRESEN2	0	Presence SW2
10	VSS		0.0V supply	42	SPKB	0	Speaker B
11	XI	I	GND	43	SPKA	0	Speaker A
12	хо	0	Open	44	FANH	0	Fan control High
13	MMOD	Ι	Memory extended mode	45	GM1	0	GM volume output 1
14	SADATO	0	SA data out	46	GM2	0	GM volume output 2
15	SADATI	Ι	SA data in	47	HPS	0	HPS filter SW
16	SACLK	0	SA clock	48	LRST	0	Serial/ Parallel IC reset
17	SBDATO	0	SB data out	49	FANL	0	Fan control LOW
18	SBDATI	Ι	SB data in	50	LDATO	0	Serial/ Parallel IC out data
19	SBCLK	Ι	SB clock	51	LCLK	0	Serial/ Parallel IC clock
20	SREQO	0	Request for the communication	52	LACH	0	Latch
			output between the sets	53	B.CON	0	Brightness control
21	RST	Ι	Reset terminal	54	RM	0	Remote control input
22	VOLCS	0	VOL. CS	55	KY1 (HPS)	Ι	Key input 1
23	VOLCLK	0	VOL. clock	56	KY2 (EACH)	Ι	Key input 2
24	VOLDATO	0	VOL. out	57	KY3 (PRO)	Ι	Key input 3
25	ATT	0	Input ATT	58	KY4 (PRE)	Ι	Key input 4
26	MUTE LED	0	Mute LED	59	KY5 (SP)	Ι	Key input 5
27	M.WAKE	Ι	Return form the stop mode	60	KY6 (PHASE)	Ι	Key input 6
28	SREQI	Ι	Request for the communication	61	VSS		(GND)
			output between the sets	62	KY7 (POWER)	Ι	Key input 7
29	INH	Ι	INM	63	KY8 (ATT)	Ι	Key input 8
30	ROT3	Ι	Rotary input 3	64	KY9 (UP)	Ι	Key input 9
31	ROT2	Ι	Rotary input 2				

## ■ NJM2903M-XE(IC454) : Temperature detector

1. Pin layout

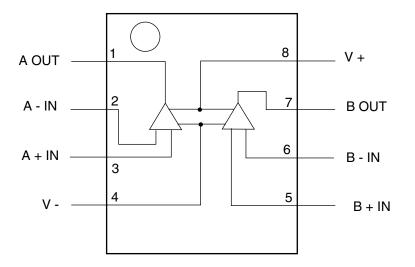


2. Block diagram

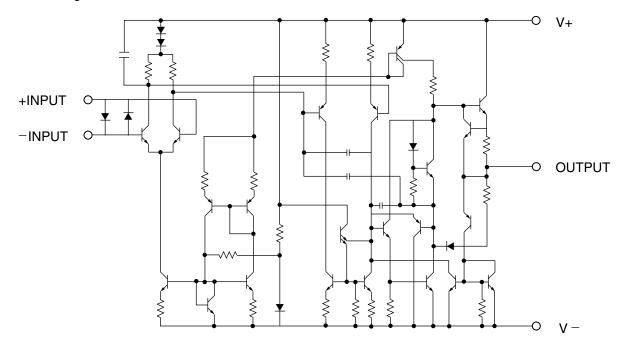


## ■ NJM5532M-D-W(IC152,153) : Presence circuit amp

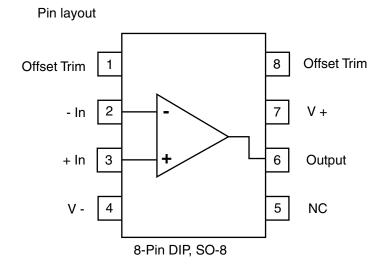
1. Pin layout



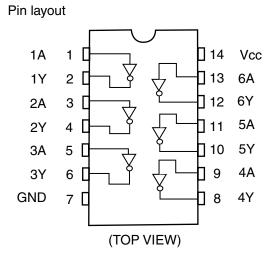
## 2. Block diagram



## ■ OPA134PA(IC108, 201, 301) : OP Amp.



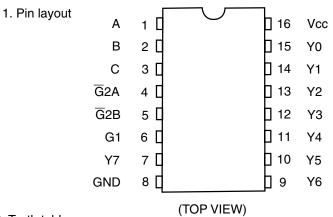
## TC74HC04AF(IC264) : Data line buffer



А	Y
L	Н
н	L

1-15

## TC74HC238AF-X(IC525) : Line recorder

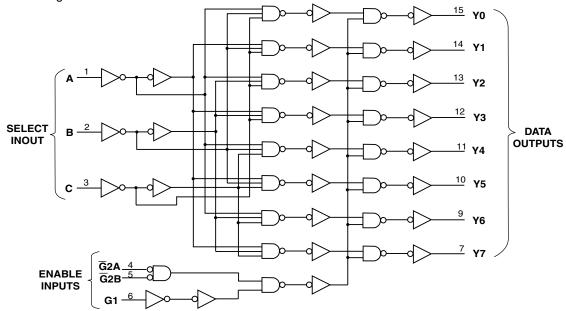


#### 2. Truth table

	INPUTS						OUTPUTS							
E	NAB	LE	S	ELEC	СТ	vo	V4	Vo	Vo	N/A	VE	Vo		SELECTED OUTPUT
G1	G2A	G2B	С	В	Α	Y0	Y1	Y2	Y3	¥4	Y5	Y6	Y7	
L	X	Х	Х	Х	Х	L	L	L	L	L	L	L	L	NONE
х	н	Х	Х	Х	Х	L	L	L	L	L	L	L	L	NONE
Х	X	Н	Х	Х	Х	L	L	L	L	L	L	L	L	NONE
н	L	L	L	L	L	Н	L	L	L	L	L	L	L	Y0
н	L	L	L	L	Н	L	Н	L	L	L	L	L	L	Y1
н	L	L	L	н	L	L	L	Н	L	L	L	L	L	Y2
н	L	L	L	Н	Н	L	L	L	н	L	L	L	L	Y3
н	L	L	Н	L	L	L	L	L	L	Н	L	L	L	Y4
Н	L	L	Н	L	Н	L	L	L	L	L	Н	L	L	Y5
н	L	L	Н	Н	L	L	L	L	L	L	L	н	L	Y6
н	L	L	Н	Н	Н	L	L	L	L	L	L	L	Н	Y7

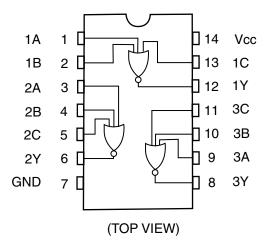
X : Don't Care

#### 3. Block diagram



## ■ TC74HC27AF-W(IC531) : Noa gate

1. Pin layout



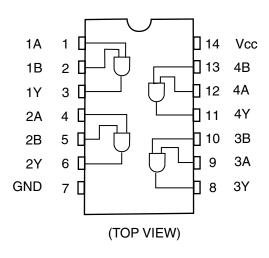
2. Truth table

Α	В	С	Y
Н	Х	X	L
Х	Н	Х	L
Х	Х	Н	L
L	L	L	Н

X : Don't Care

## ■ TC74VHC08F-X(IC281) : Dataline buffer

1. Terminal layout



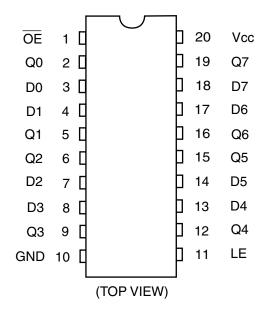
#### 2. Truth table

Α	В	Y
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

X : Don't Care

## TC74HC373HF-X(IC522, IC523, IC524) : Shift register for relay control

1. Pin layout



2. Truth table

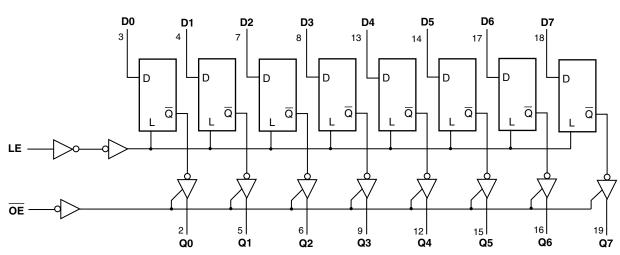
ſ	II	OUTPUTS		
	ŌE	LE	D	Q
	Н	Х	Х	Z
	L	L	Х	Qn
	L	Н	L	L
	L	Н	Н	Н

X : Don't Care

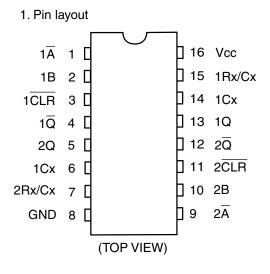
Z : High impedance

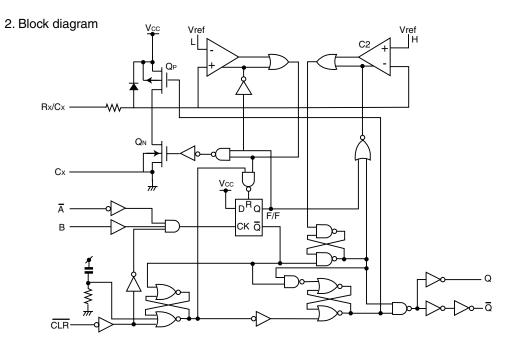
Qn: The Q output level immediately before the LE becomes to "L".

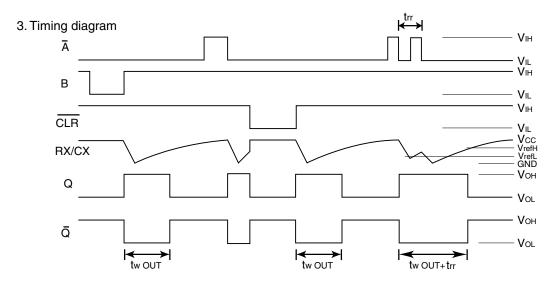
3. Block diagram



## TC74VHC123AF-X(IC532) : Monostable multivibrator

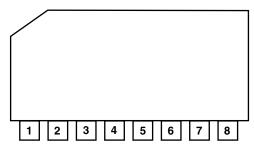




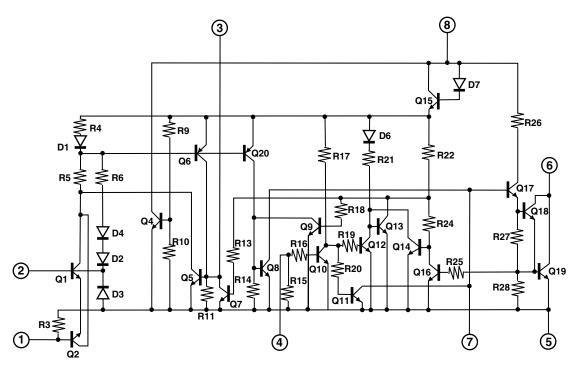


## UPC1237HA (IC453) : Protection IC

1. Pin layout



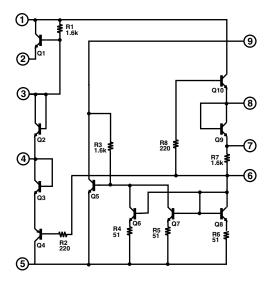
2. Block diagram



AX-M9000

## ■ VC5022-2(IC302) : Advanced super A

Block diagram



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AX-M9000
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